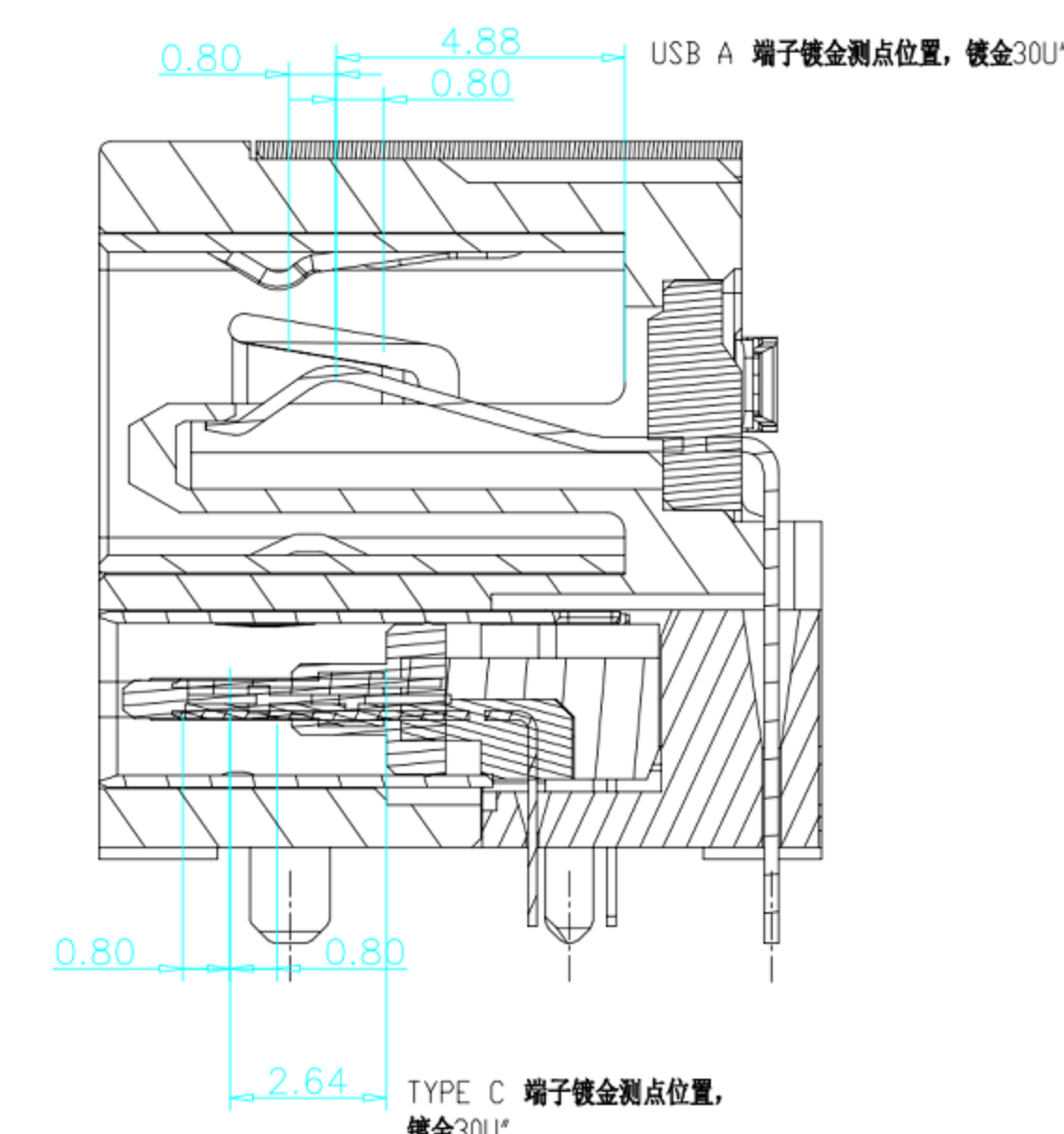
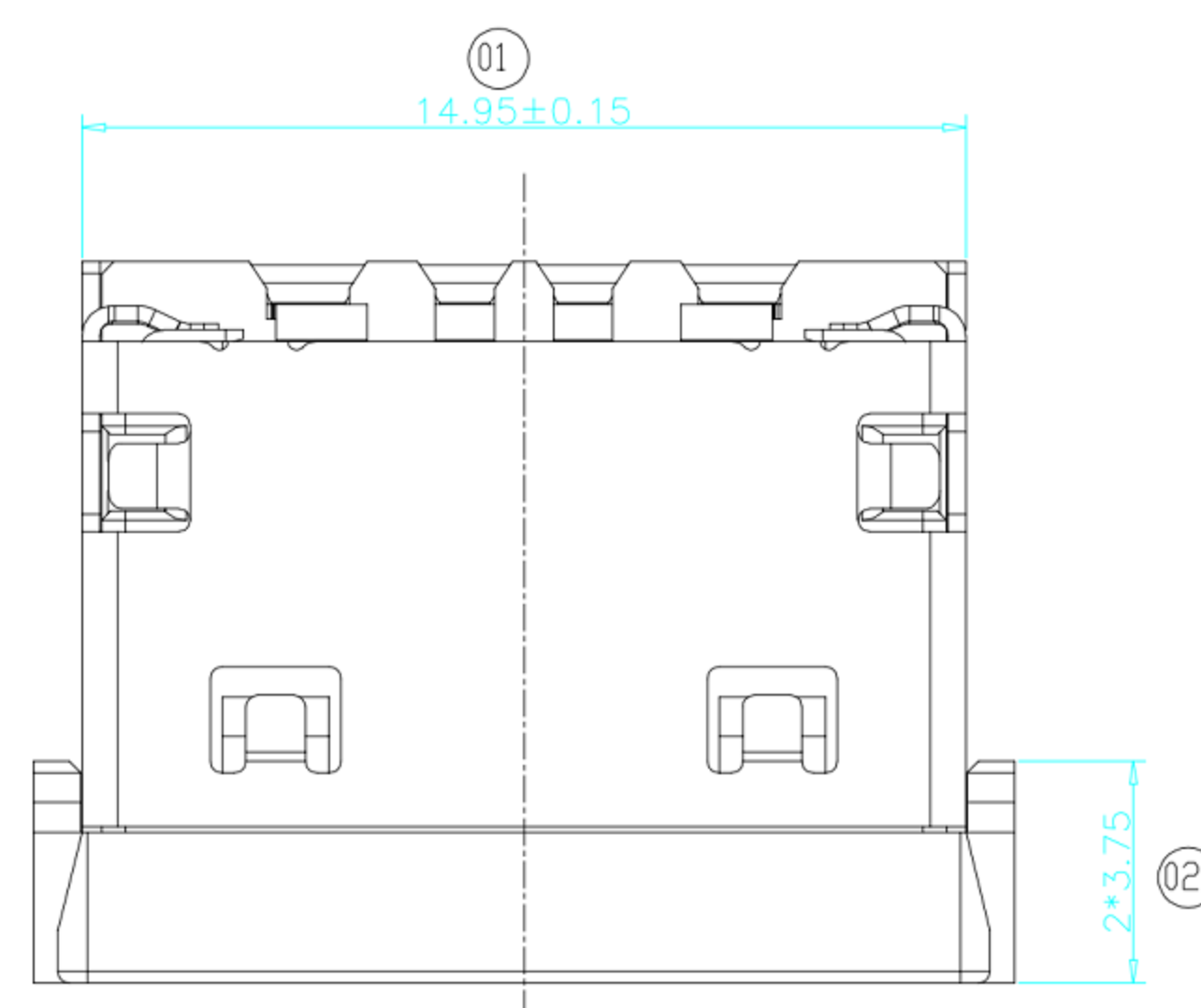
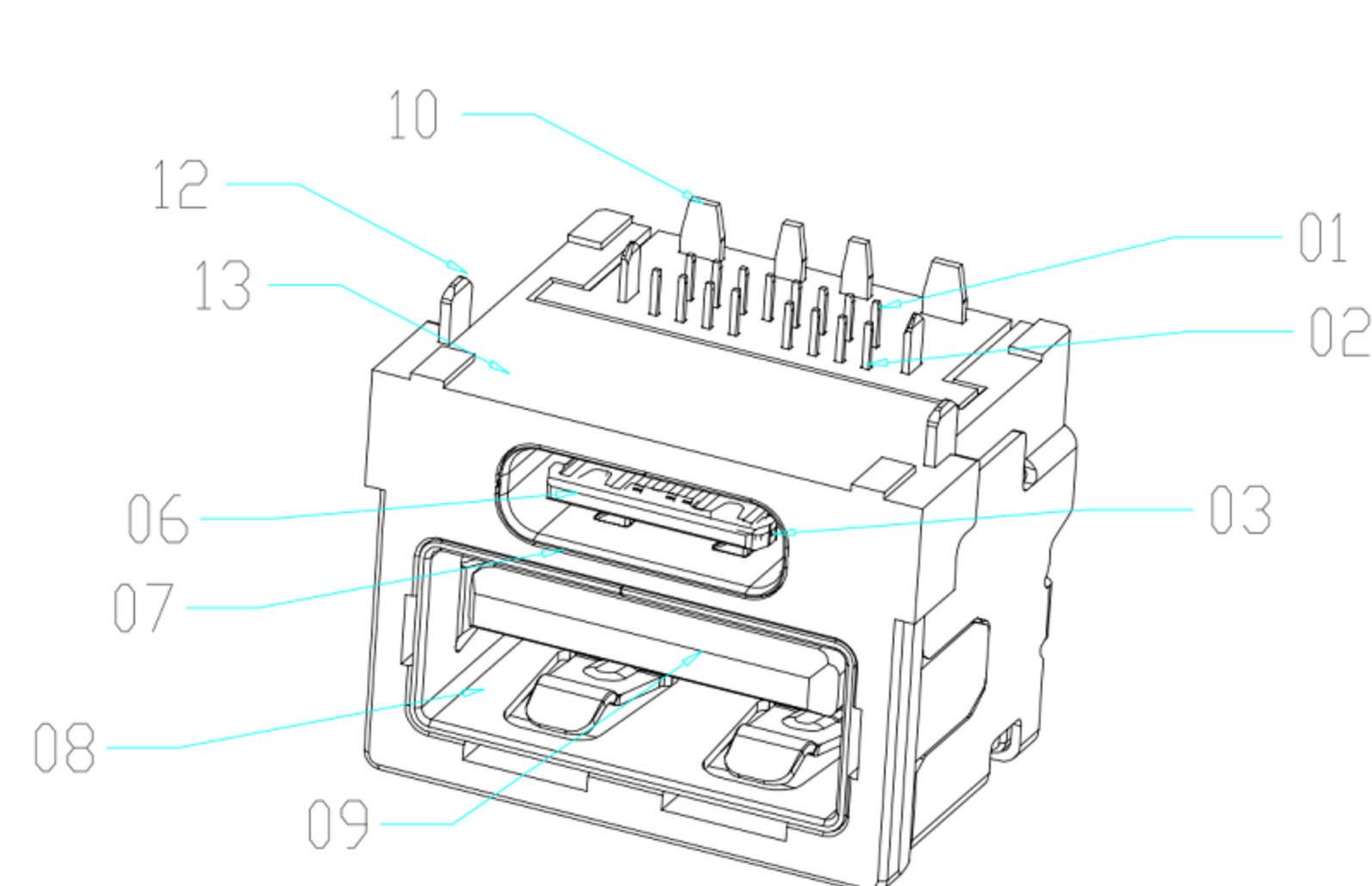
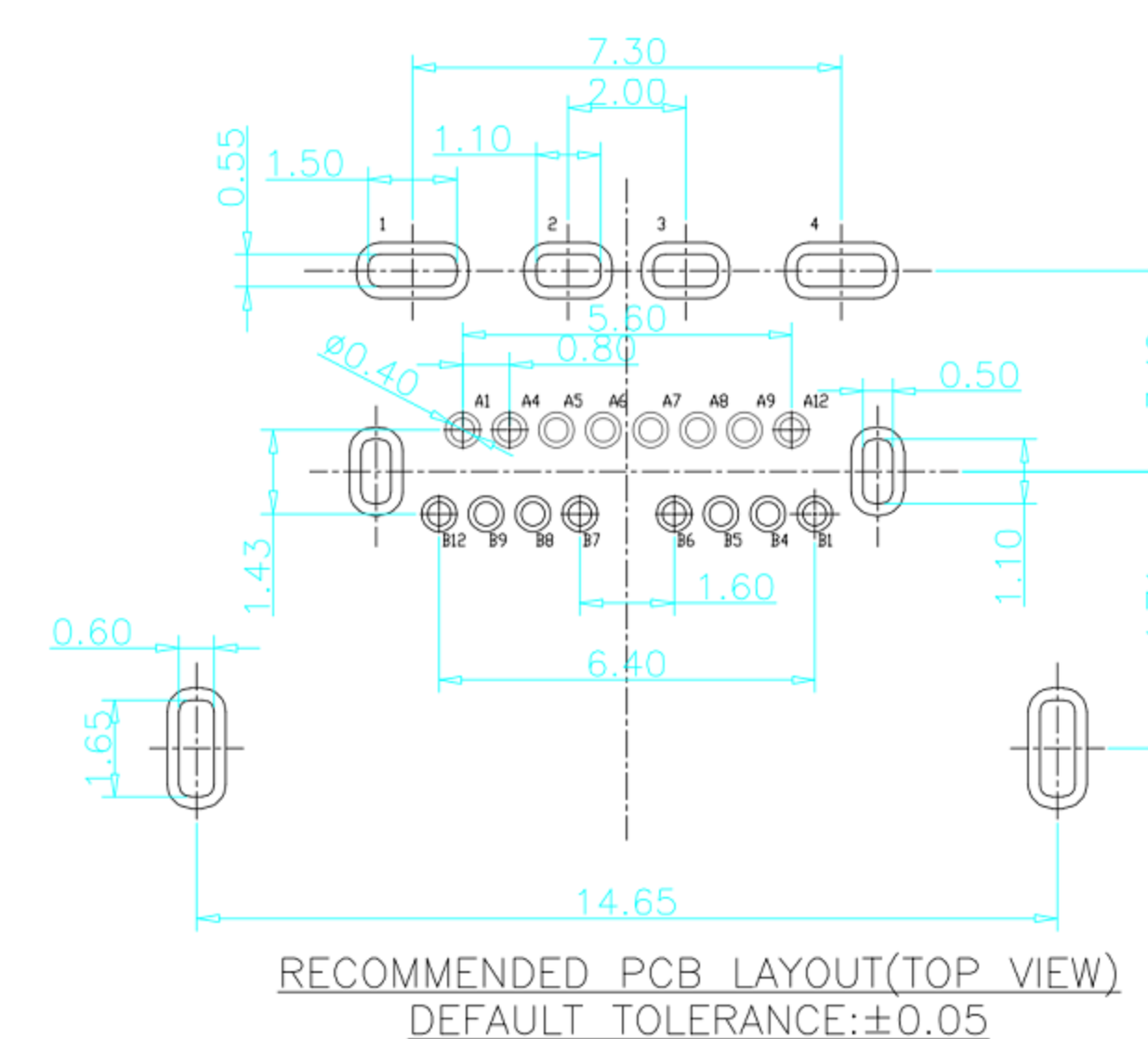
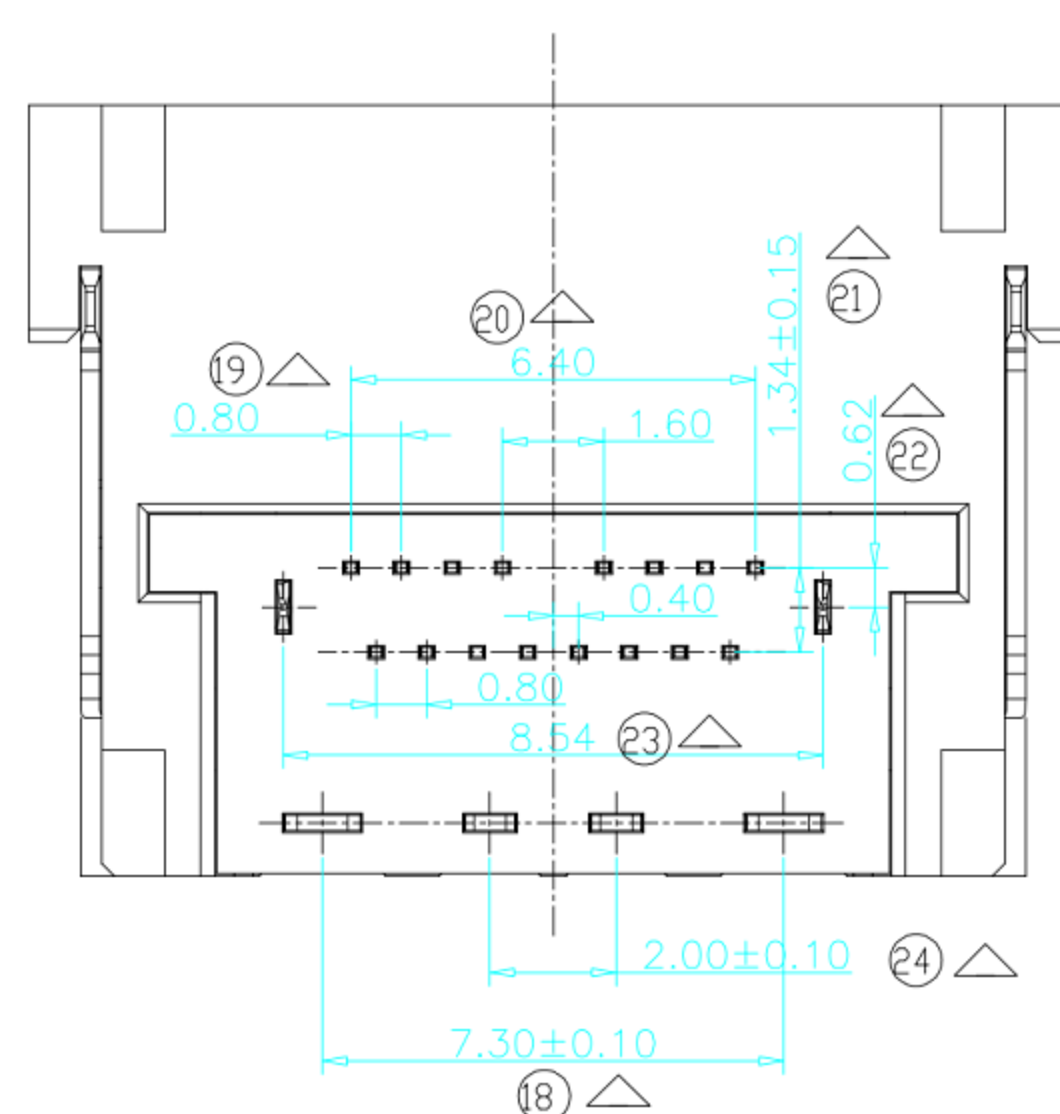
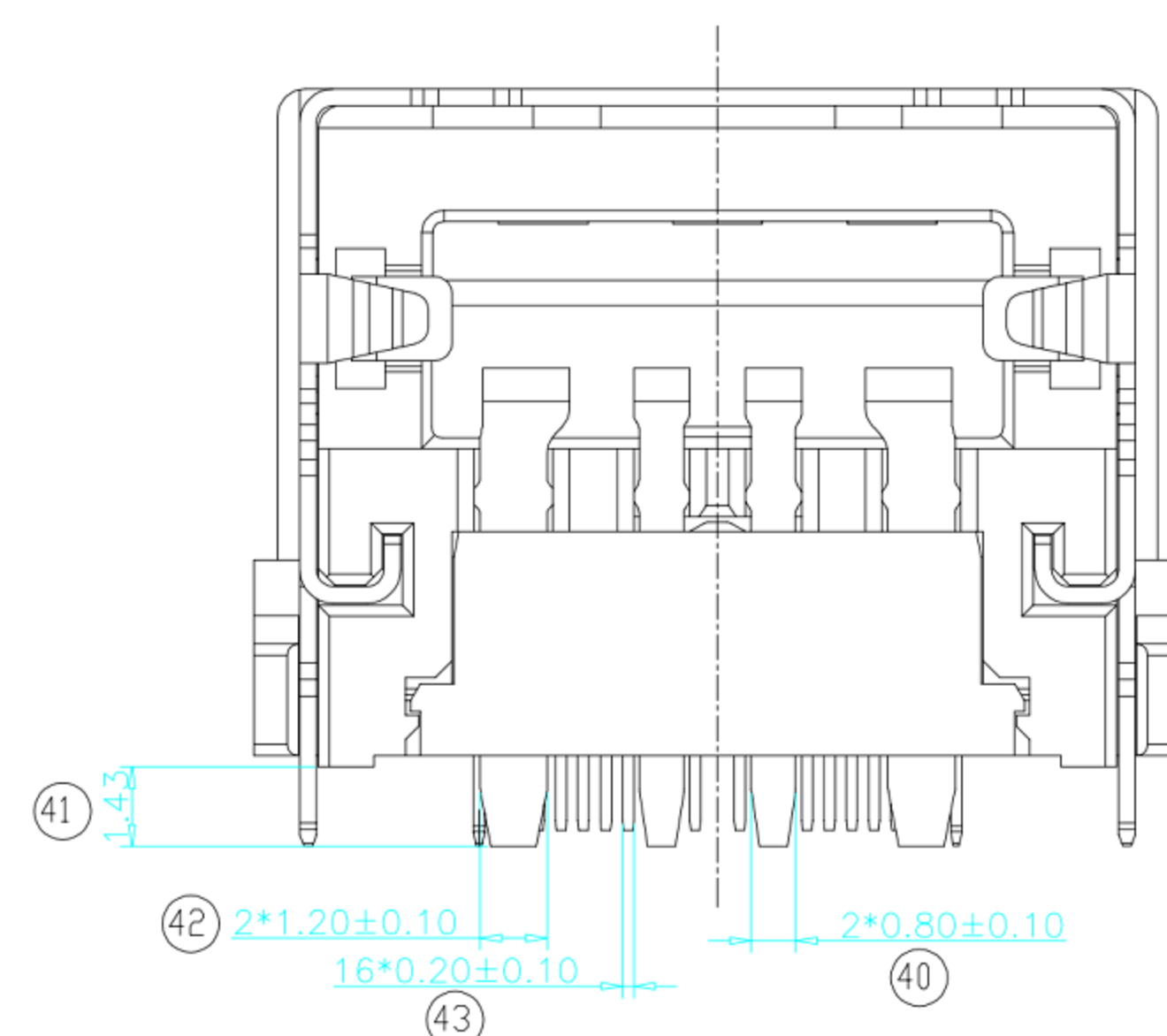
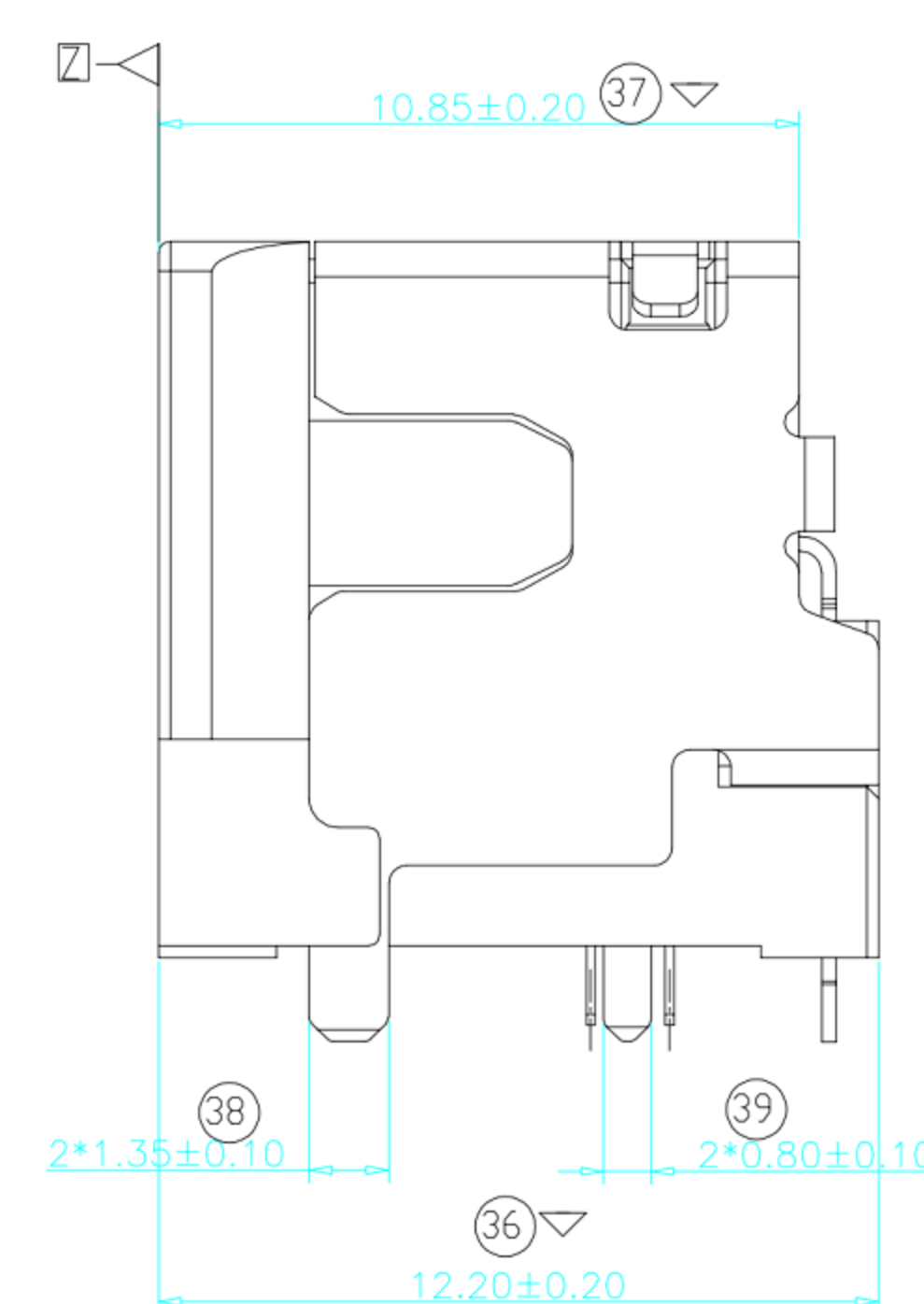
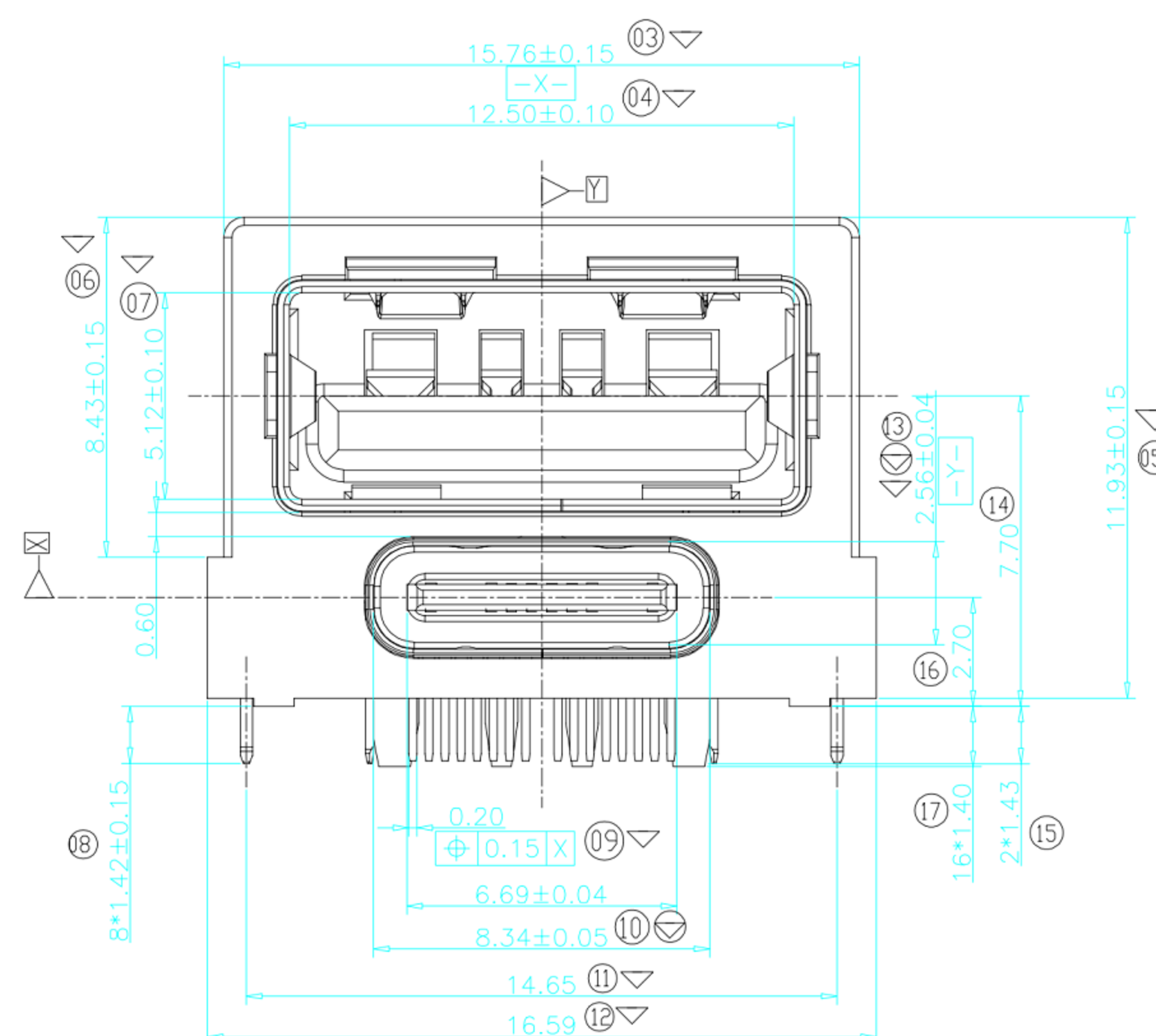
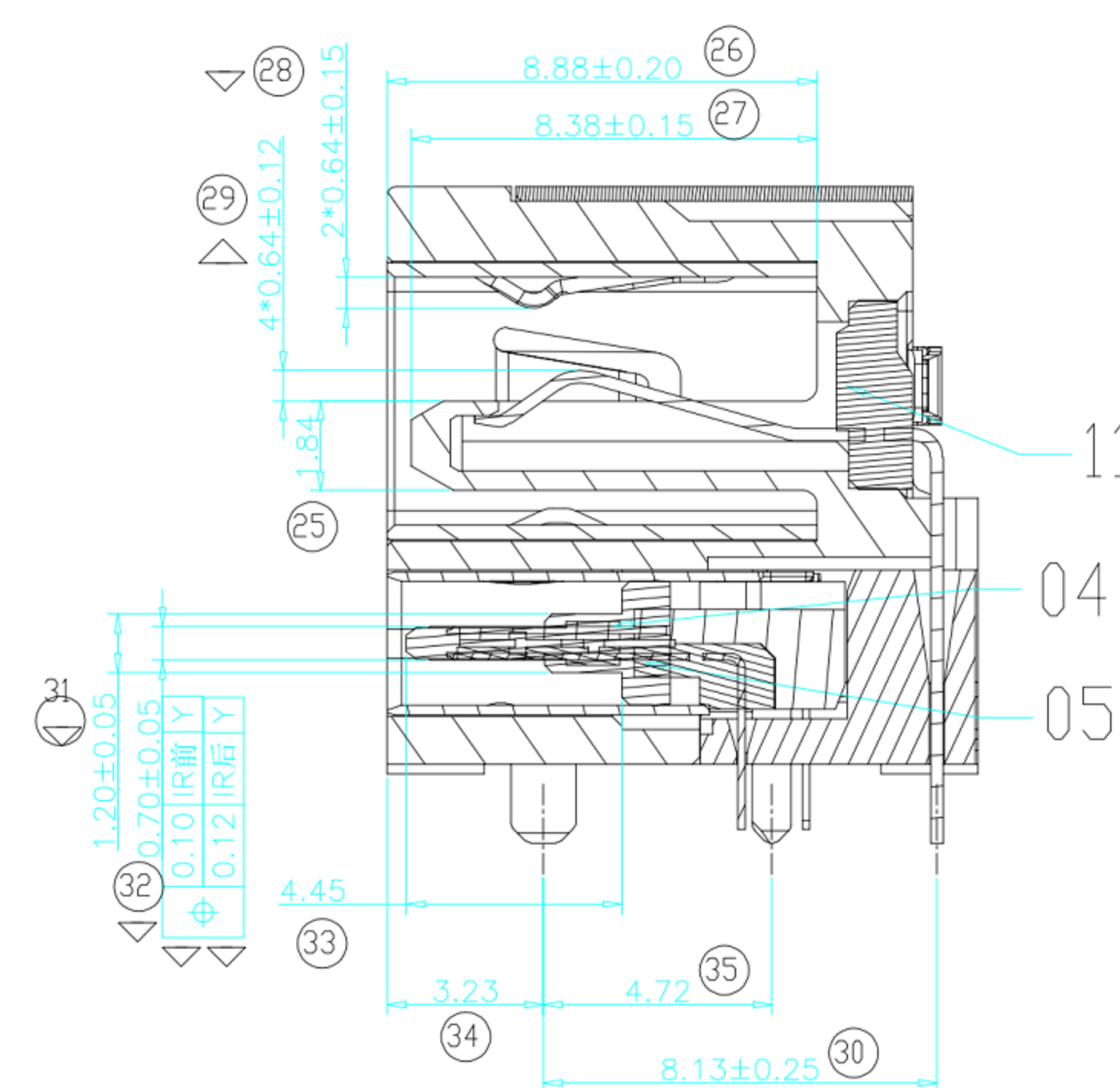




端子膜厚测试区示意图



SPECIFICATIONS

Electrical:

- 1.Rating: 3.0 A
- 2.Dielectric Withstand Voltage: 100V AC
- 3.Contact Resistance: 40 mΩ Max
- 4.Insulation Resistance: 100 MΩ Min

Mechanical:

- 1.Connector Mate and Unmate Force
 - 1.1 Mate force: 5~20 N
 - 1.2 Unmate force: 8~20 N

Material:

- 1.Housing: LCP
- 2.Contact: Copper Alloy
- 3.Shell: SUS

Finish:

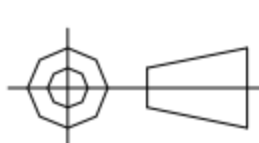
- 1.Contact: Plated Gold in Mating Area ;
Tin Plated on Solder Balls ;
Nickel under plated overall
- 2.Shell: Nickel under Plated surface layer



東莞市高迪電子有限公司
DONG GUAN GAO DI ELECTRONICS CO., LTD

TOLERANCE UNLESS OTHERWISE STATED :	Up to 5	± 0.2
	Above 5 ~ 15	± 0.3
	Above 15 ~ 30	± 0.4
	Above 30 ~ 50	± 0.5
	Angle	± 0.3°

3RD.
ANGEL'S



UNITS

MM

DRAWN BY:	DATE
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Jack Lu	05/13/2
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CHECKED BY:	DATE
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Jacky Chen	05/13/2025
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APPROVED BY:	DATE:
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Tony Kao	05/13/2017
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MAT'L

FINISH

SCALE

SHEET NO.

TITLE

MODEL

DWG N

PART N

CONNECTOR

USB C TYPE

GD-CMB-16F-UA-ET SERIES

	SIZE
--	------

A4

VER
D

	$\mathbf{K}$
--	--------------

Pin Assignments(C端) :

Pin	Signal Name	Description	Mating Sequence	Pin	Signal Name	Description	Mating Sequence
A1	GND	Ground return	First	B12	GND	Ground return	First
A4	VBUS	Bus Power	First	B9	VBUS	Bus Power	First
A5	CC1	Configuration Channel	Second	B8	SB2	Sideband Use	Second
A6	Dp1	Positive half of the USB2.0 differential pair-Position 1	Second	B7	Dn2	Negative half of the USB2.0 differential pair-Position 2	Second
A7	Dn1	Negative half of the USB2.0 differential pair-Position 1	Second	B6	Dp2	Positive half of the USB2.0 differential pair-Position 2	Second
A8	SB1	Sideband Use	Second	B5	CC2	Configuration Channel	Second
A9	VBUS	Bus Power	First	B4	VBUS	Bus Power	First
A12	GND	Ground return	First	B1	GND	Ground return	First

Pin Assignments(A端) :

Pin	Signal Name	Description
1	GND	Ground return
2	D+	Positive half of the USB2.0 differential pair-Position 1
3	D-	Negative half of the USB2.0 differential pair-Position 2
4	VBUS	Bus Power



鍍層厚度 :

Blank : 1u"
2 : 15u"
3 : 30u"

SPECIFICATIONS

Electrical:

- 1.Rating: 3.0 A
- 2.Dielectric Withstand Voltage: 100V AC
- 3.Contact Resistance: 40 mΩ Max
- 4.Insulation Resistance: 100 MΩ Min

Mechanical:

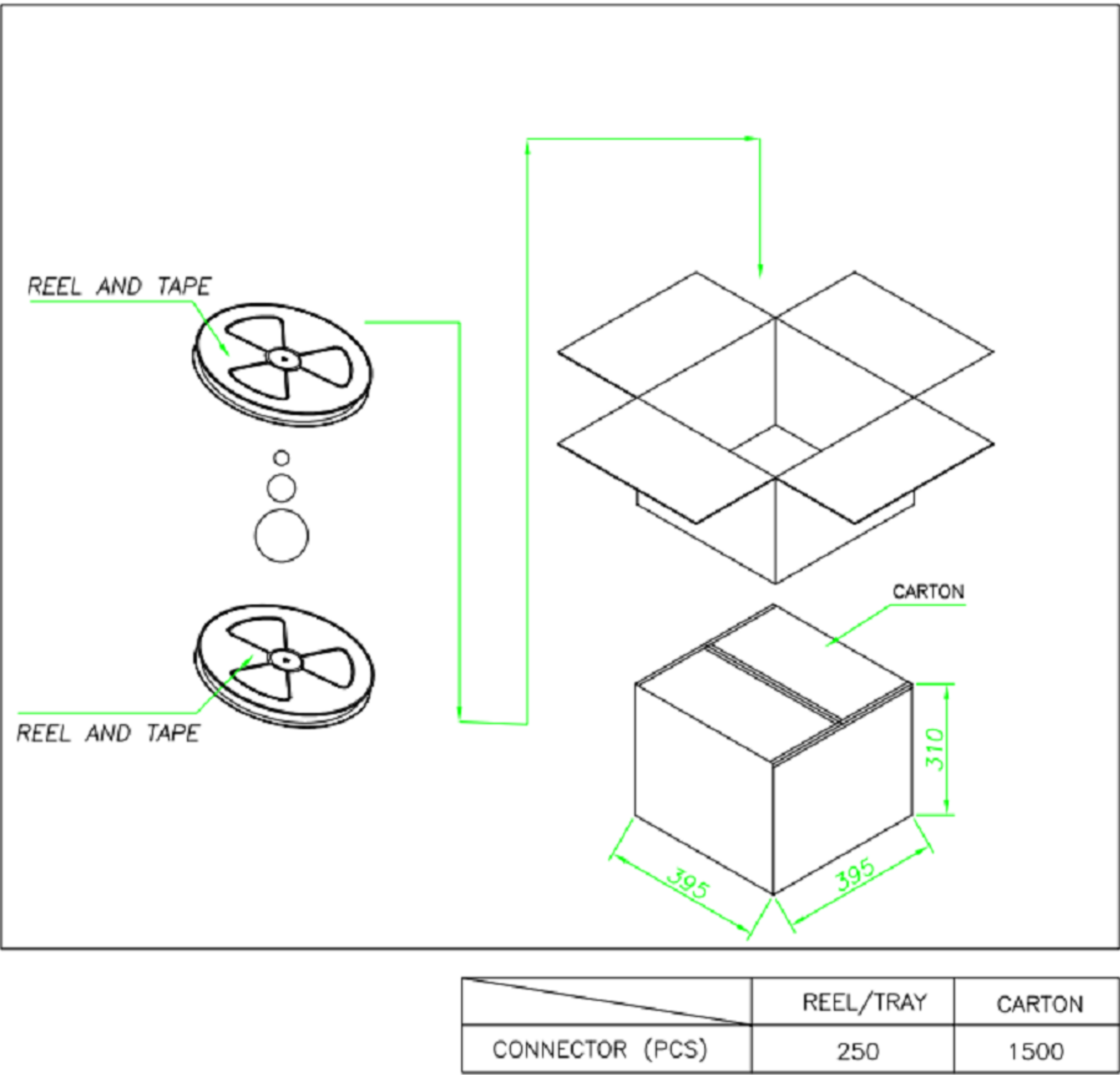
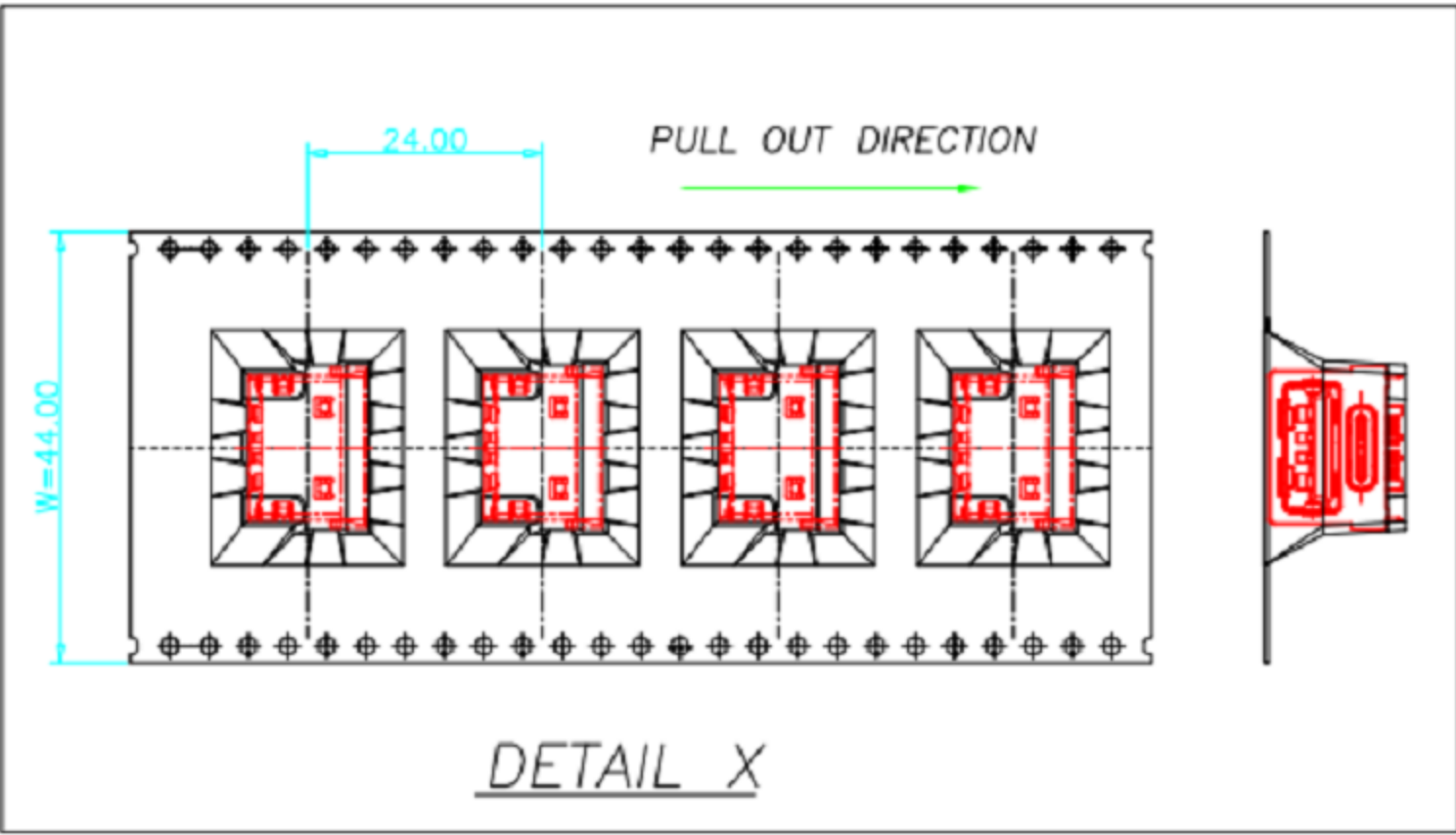
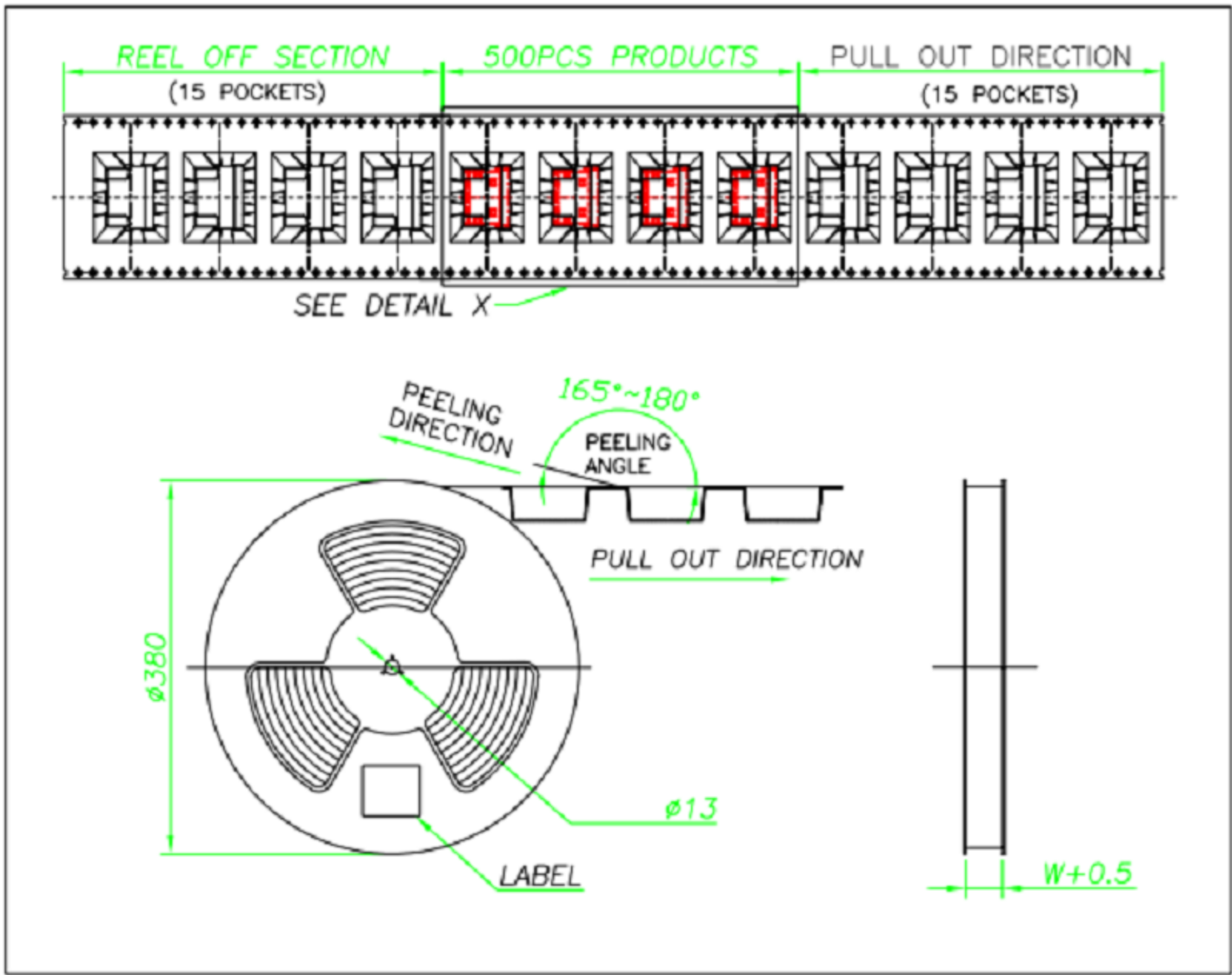
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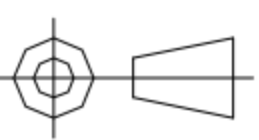


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DONG GUAN GAO DI ELECTRONICS CO., LTD

TOLERANCE UNLESS
OTHERWISE STATED :

Up to 5 ±0.2
Above 5 ~ 15 ±0.3
Above 15 ~ 30 ±0.4
Above 30 ~ 50 ±0.5
Angle ±0.3°

3RD.
ANGEL'S



UNITS

MM

DRAWN BY: DATE

Jack Lu 05/13/20

CHECKED BY: DATE

Jacky Chen 05/13/20

APPROVED BY: DATE

Tony Kao 05/13/20

MAT'L

FINISH

SCALE

SHEET NO.

TITLE

MODLE

DWG NO.

PART NO.

CONNECTOR

USB C TYPE

SIZE

A4

VER

R